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Cebenko 1-1-3-1-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): S.R. Cebenko et al.
Case: 1-1-3-1-1
Serial No.: 10/082,050
Filing Date: February 14, 2002
Group: 2836
Examiner: To Be Assigned

Title: Integrated Circuit Base Transistor Structure and Associated Programmable Cell Library

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TRANSMITTAL OF FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Attention: Official Draftsperson

Sir:

In response to the Notice to File Corrected Application Papers dated March 19, 2002, Applicants submit herewith four (4) sheets of formal drawings to complete the above-referenced patent application.

A copy of the Notice is enclosed herewith. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

Joseph B. Ryan

Date: May 7, 2002

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